

FIG. 1 is a block diagram of a network system 100. The network system 100 includes a base station 102, a network infrastructure 104, and a plurality of user devices 106, 108, 110, 112, 114, 116, 118, 120, 122. The base station 102 is connected to the network infrastructure 104. The user devices 106, 108, 110, 112, 114, 116, 118, 120, 122 are connected to the base station 102 via wireless communication links.

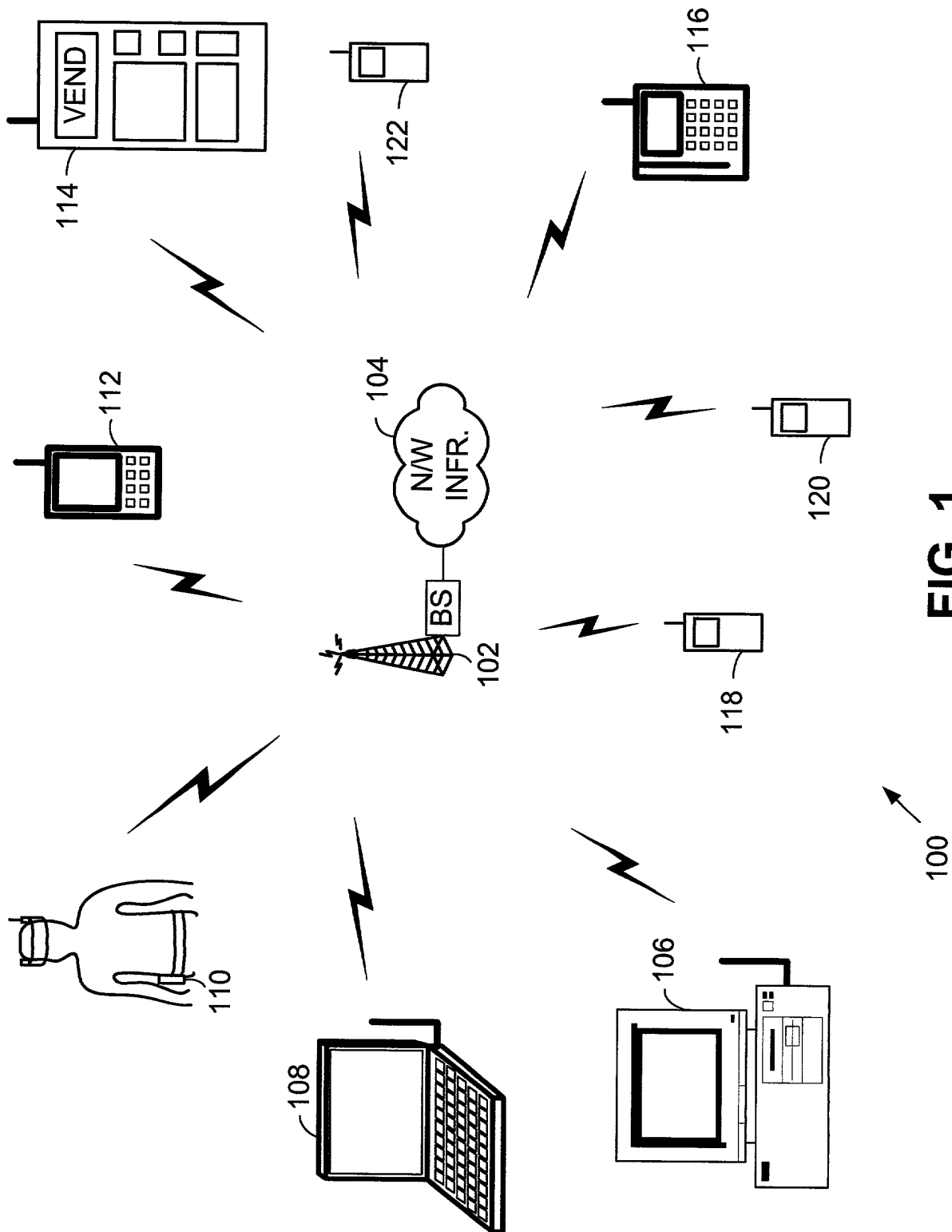


FIG. 1



FIG. 2

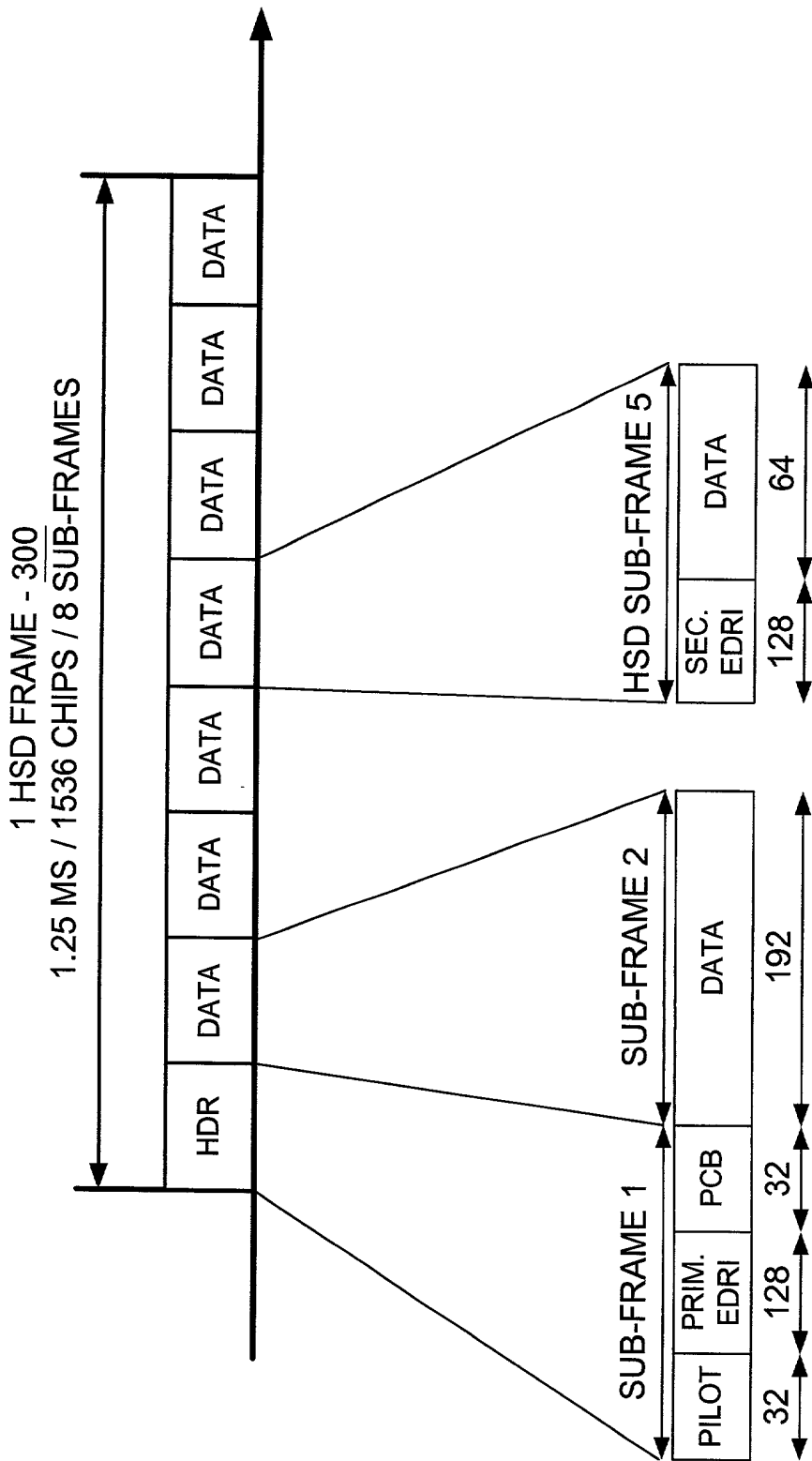


FIG. 3

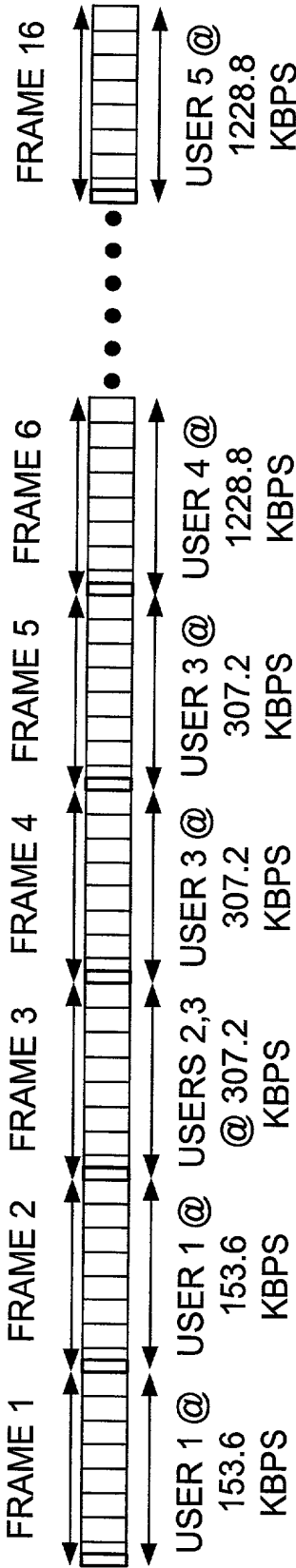


FIG. 4A

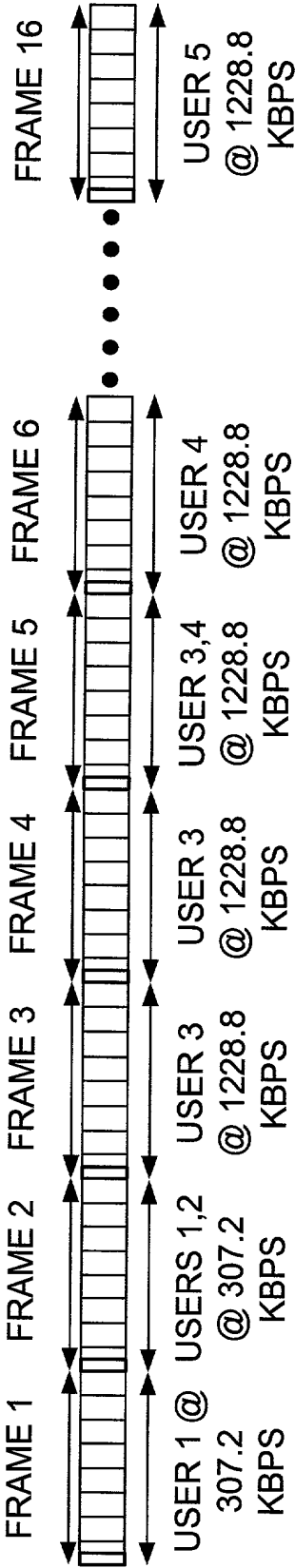


FIG. 4B

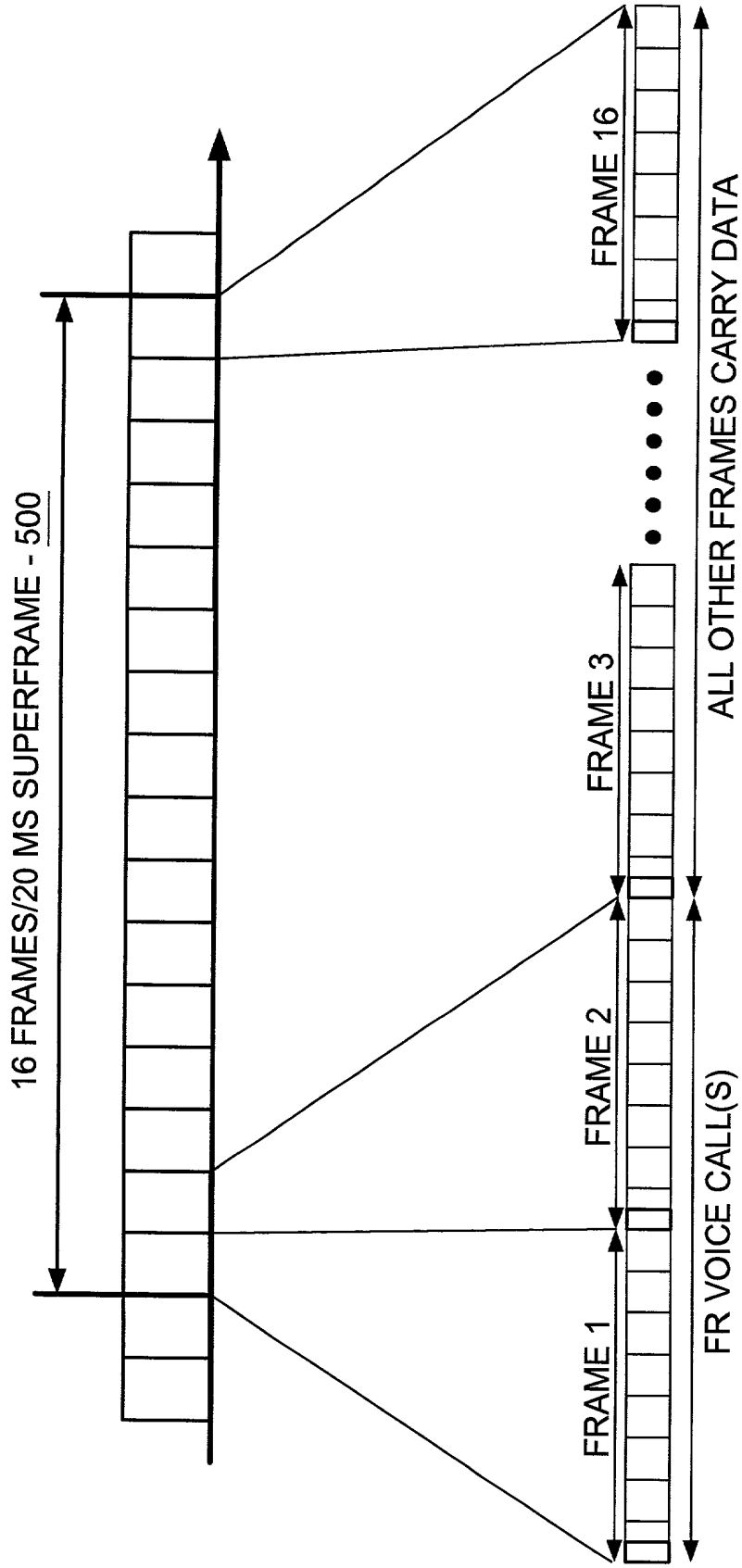
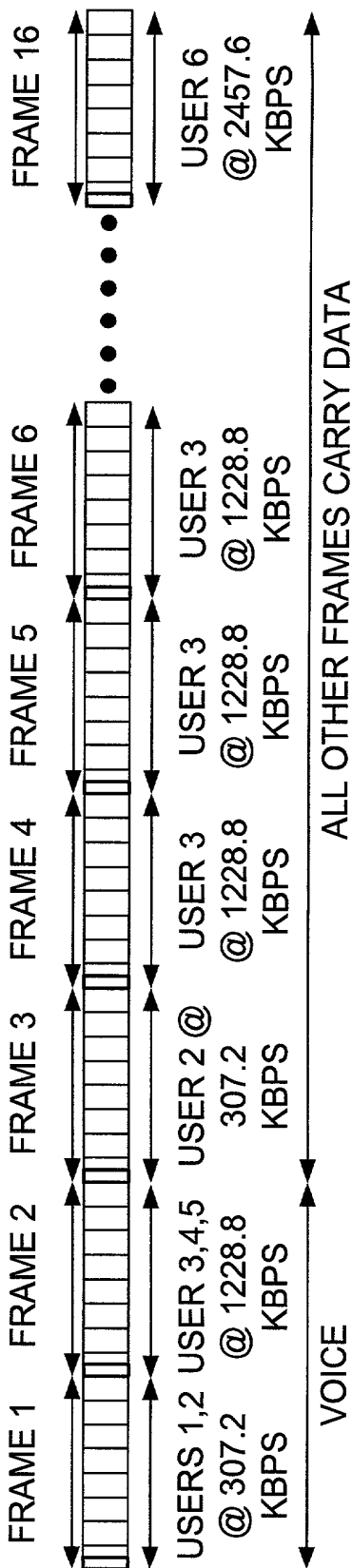
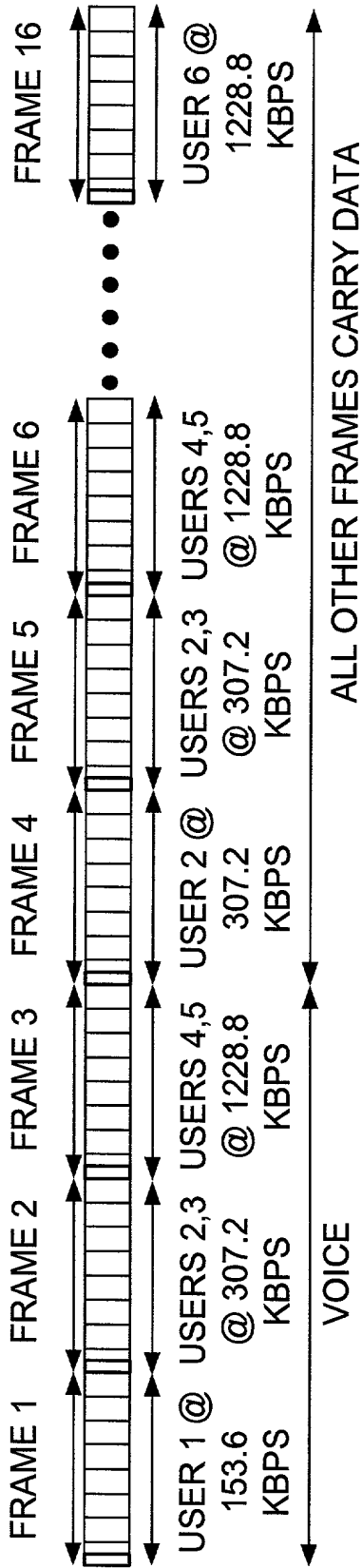


FIG. 5



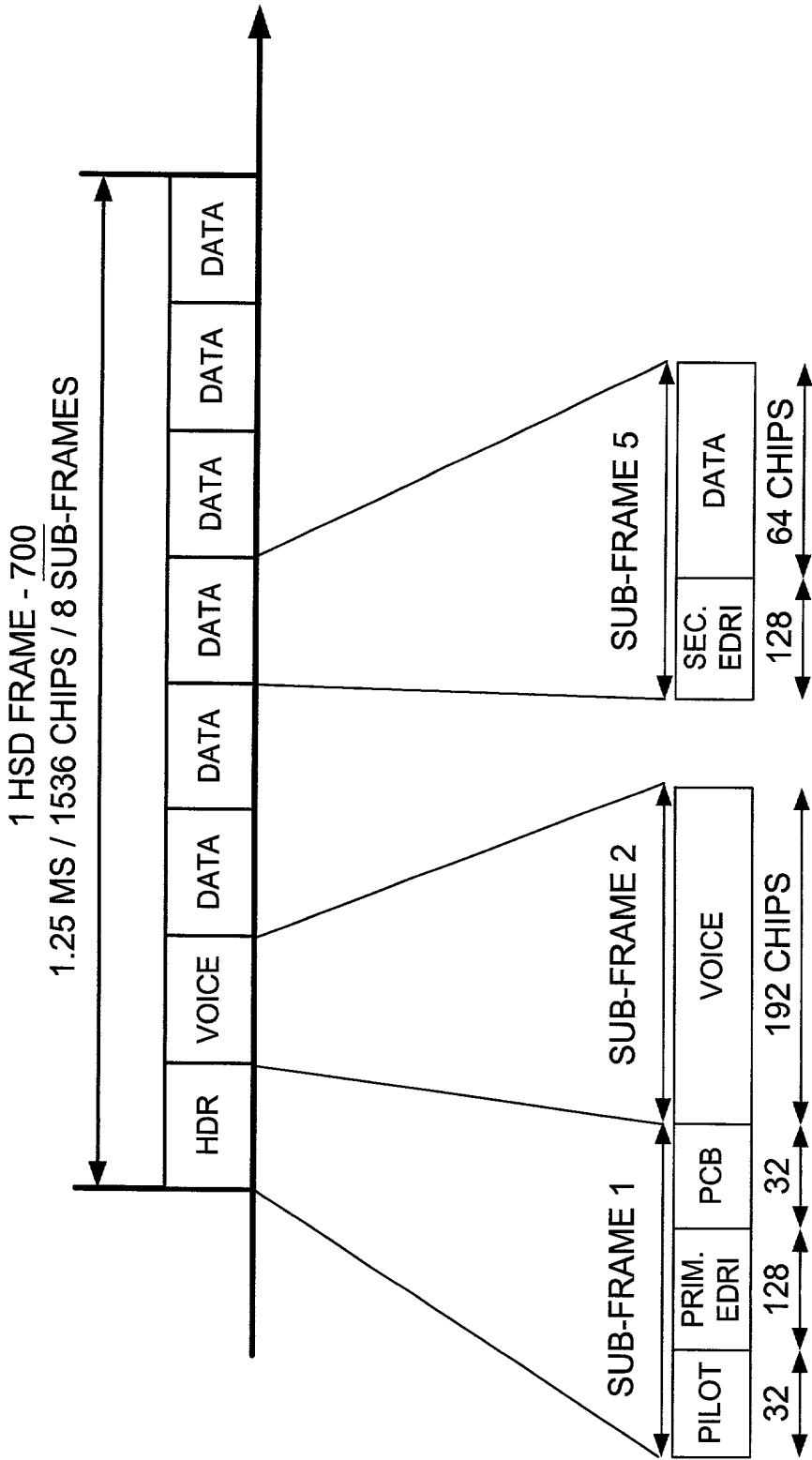
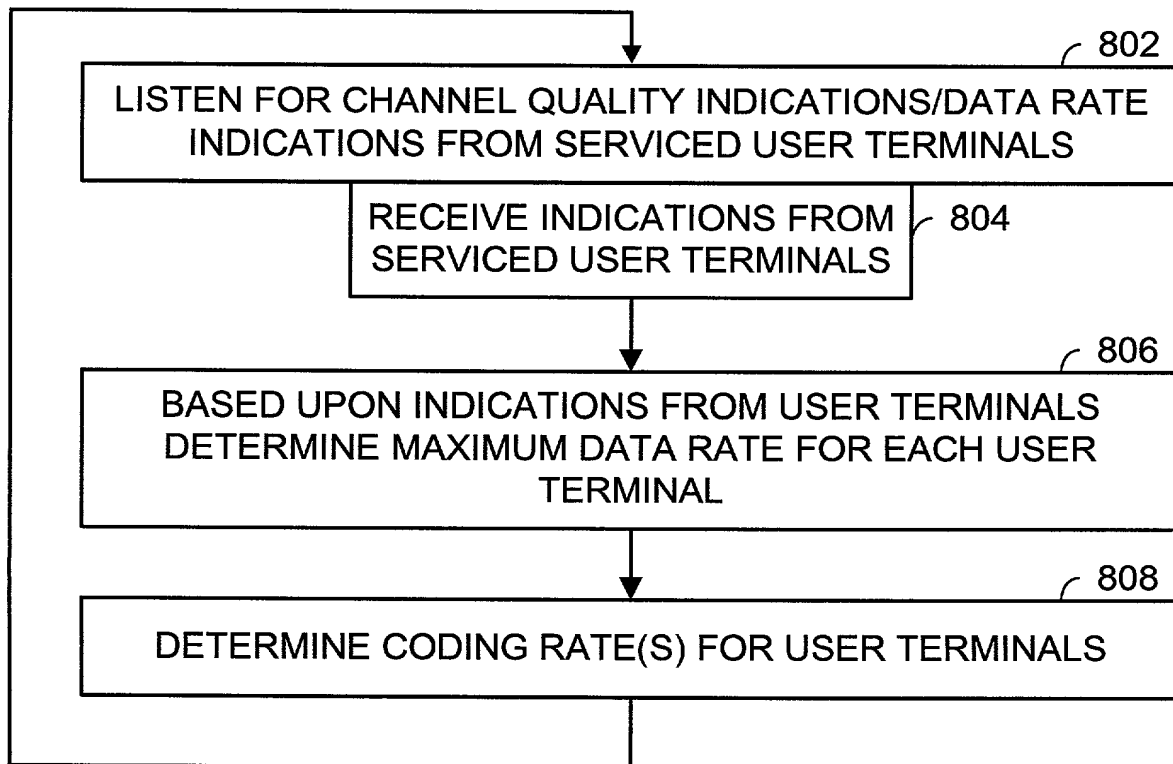


FIG. 7

**FIG. 8**

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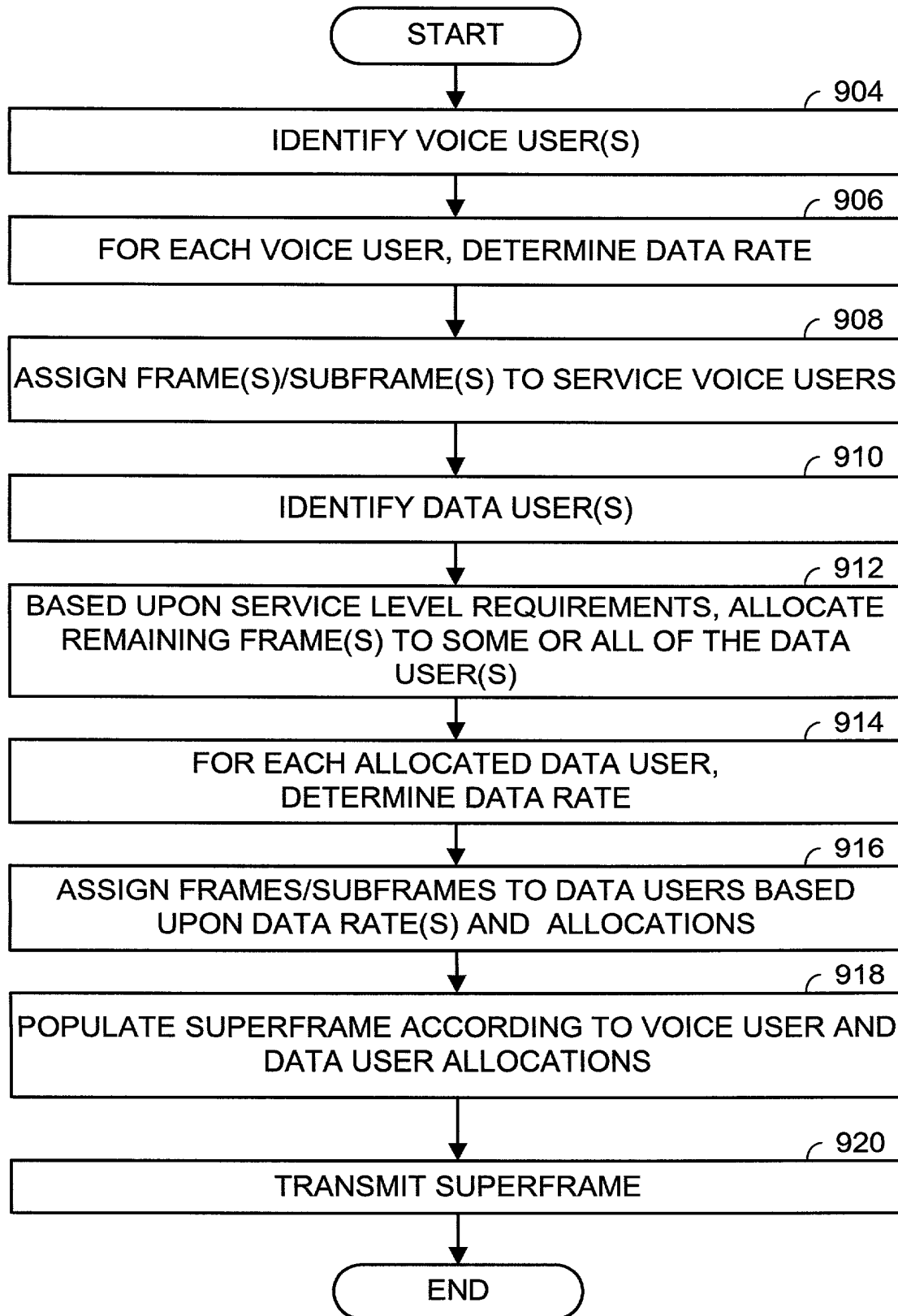
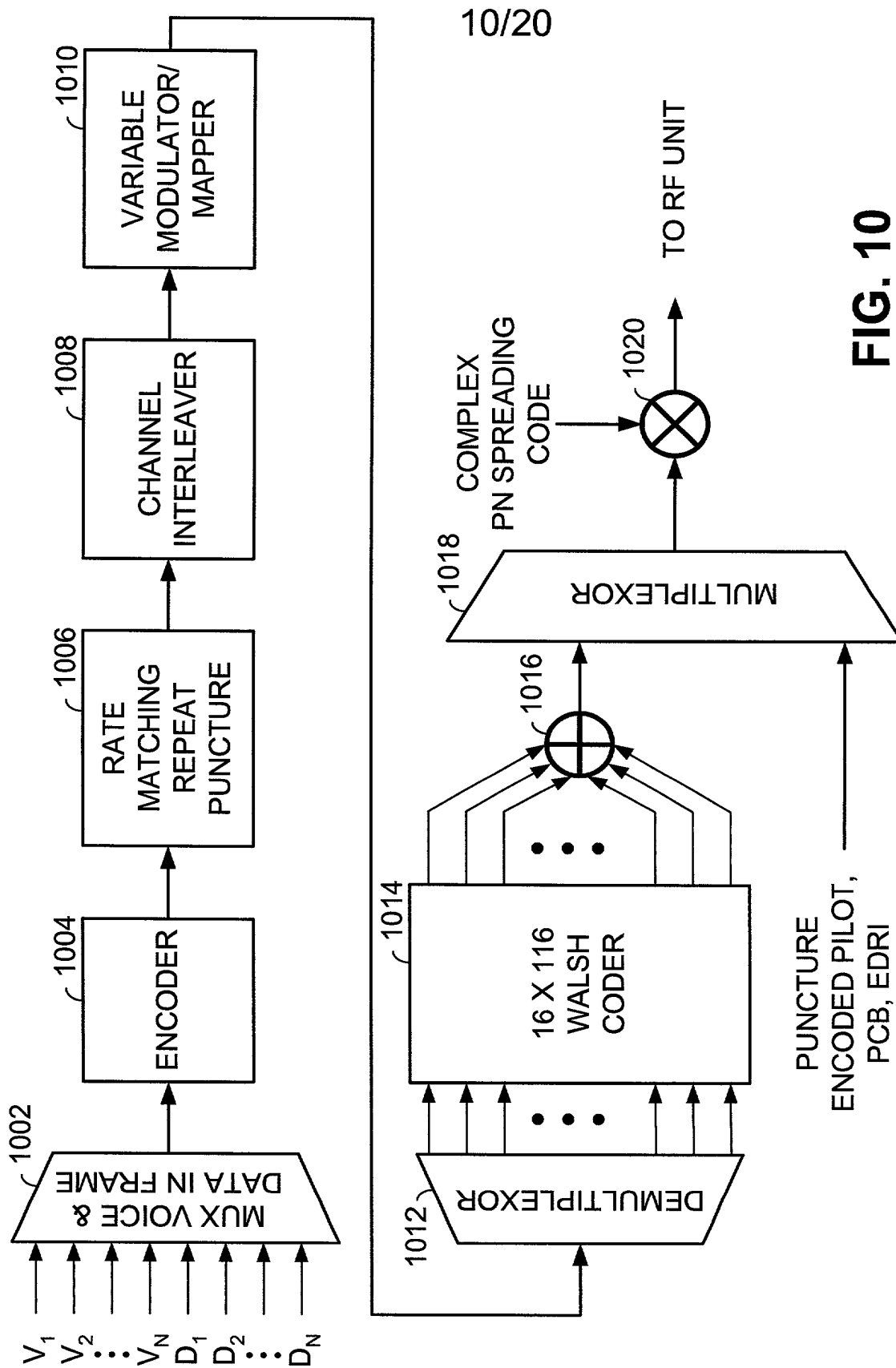


FIG. 9



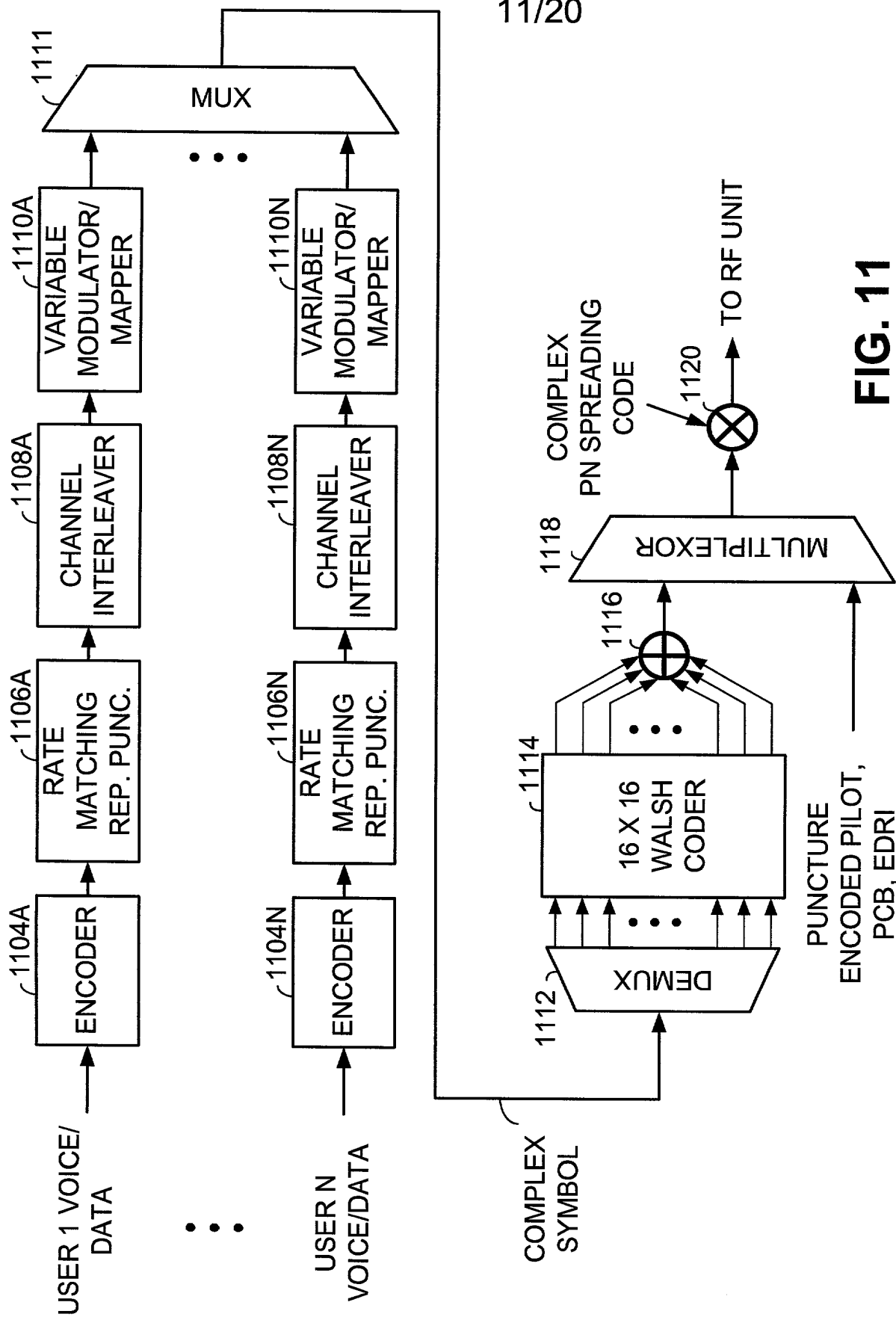


FIG. 11

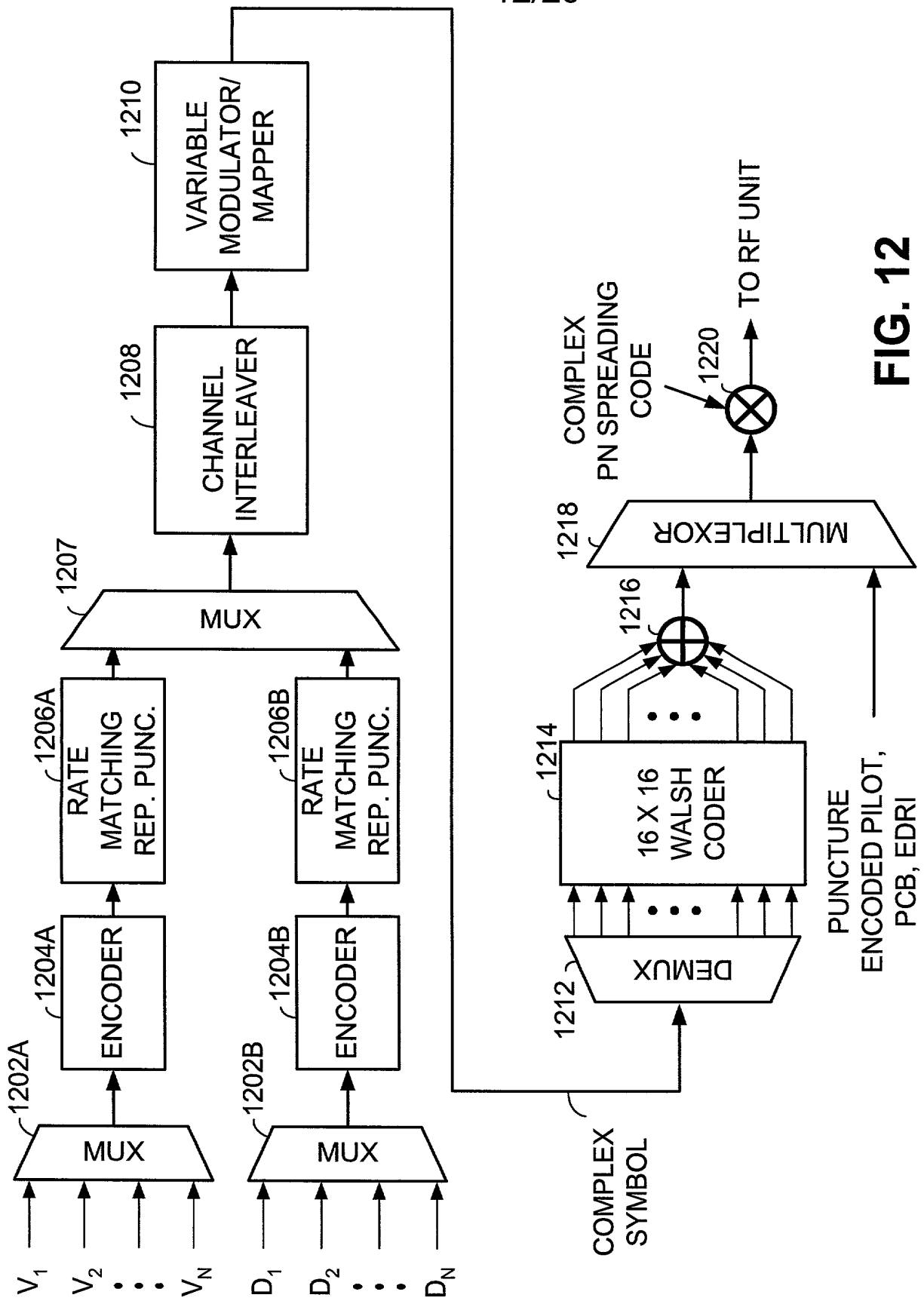


FIG. 12

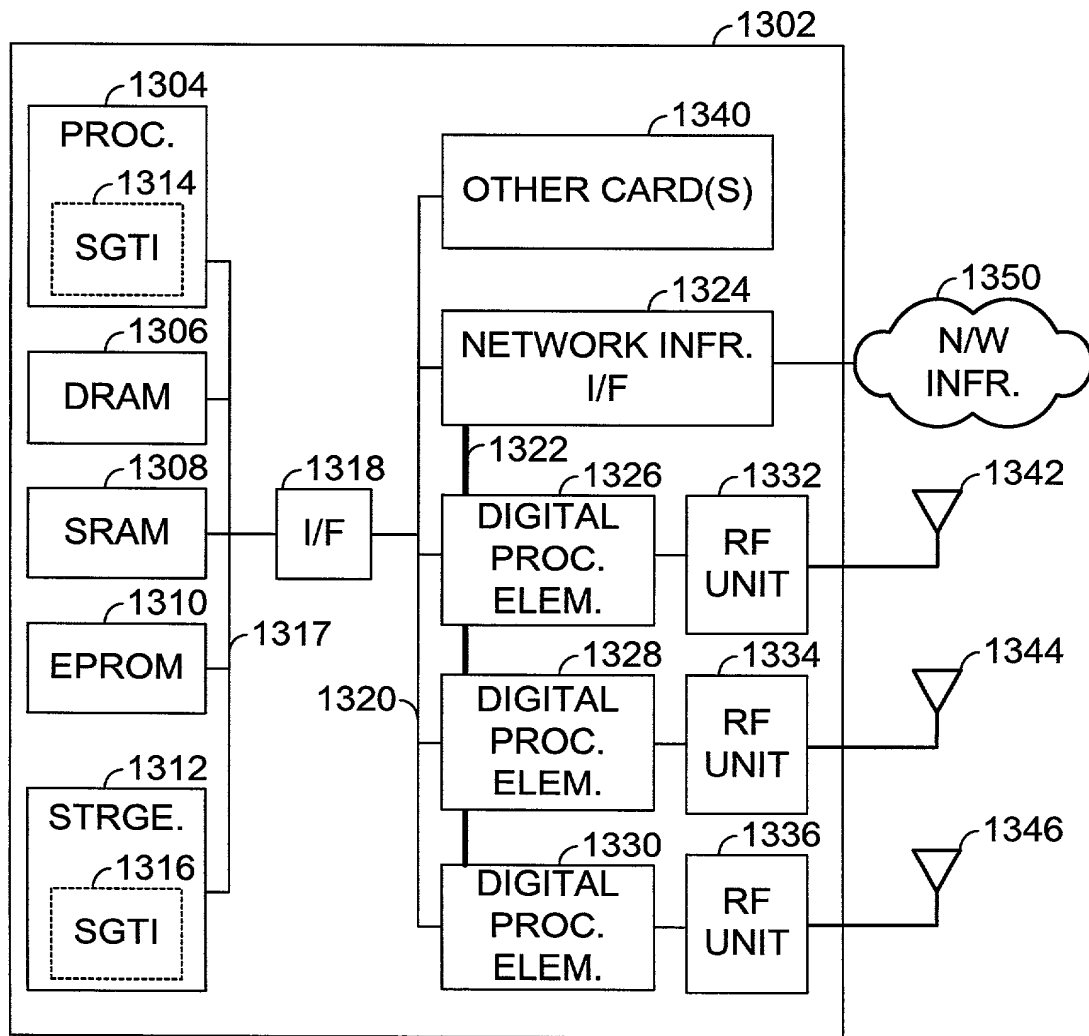


FIG. 13

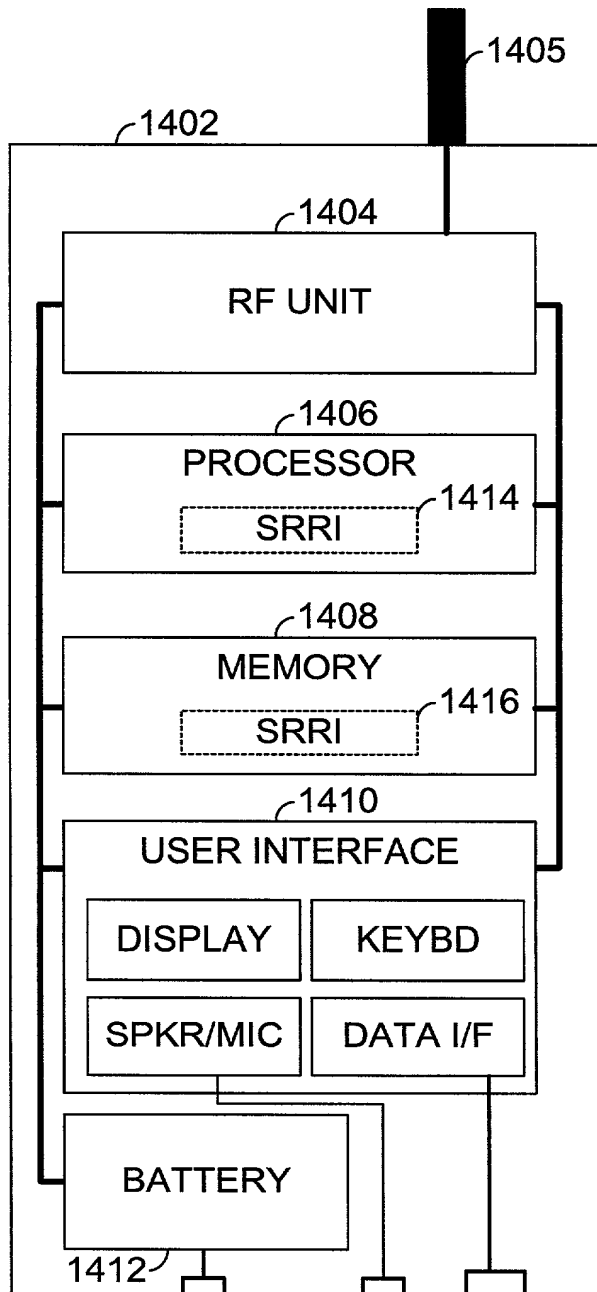


FIG. 14

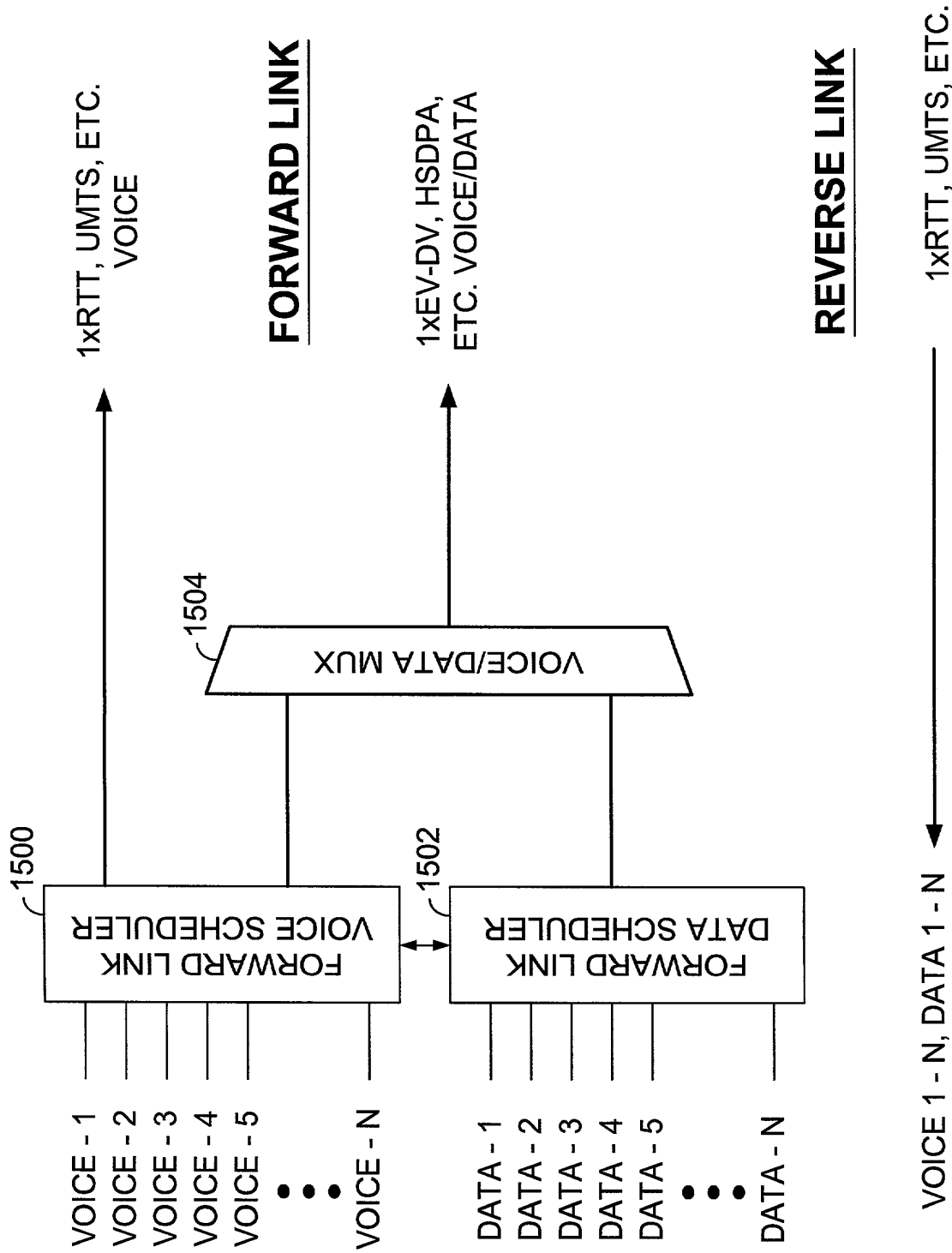


FIG. 15

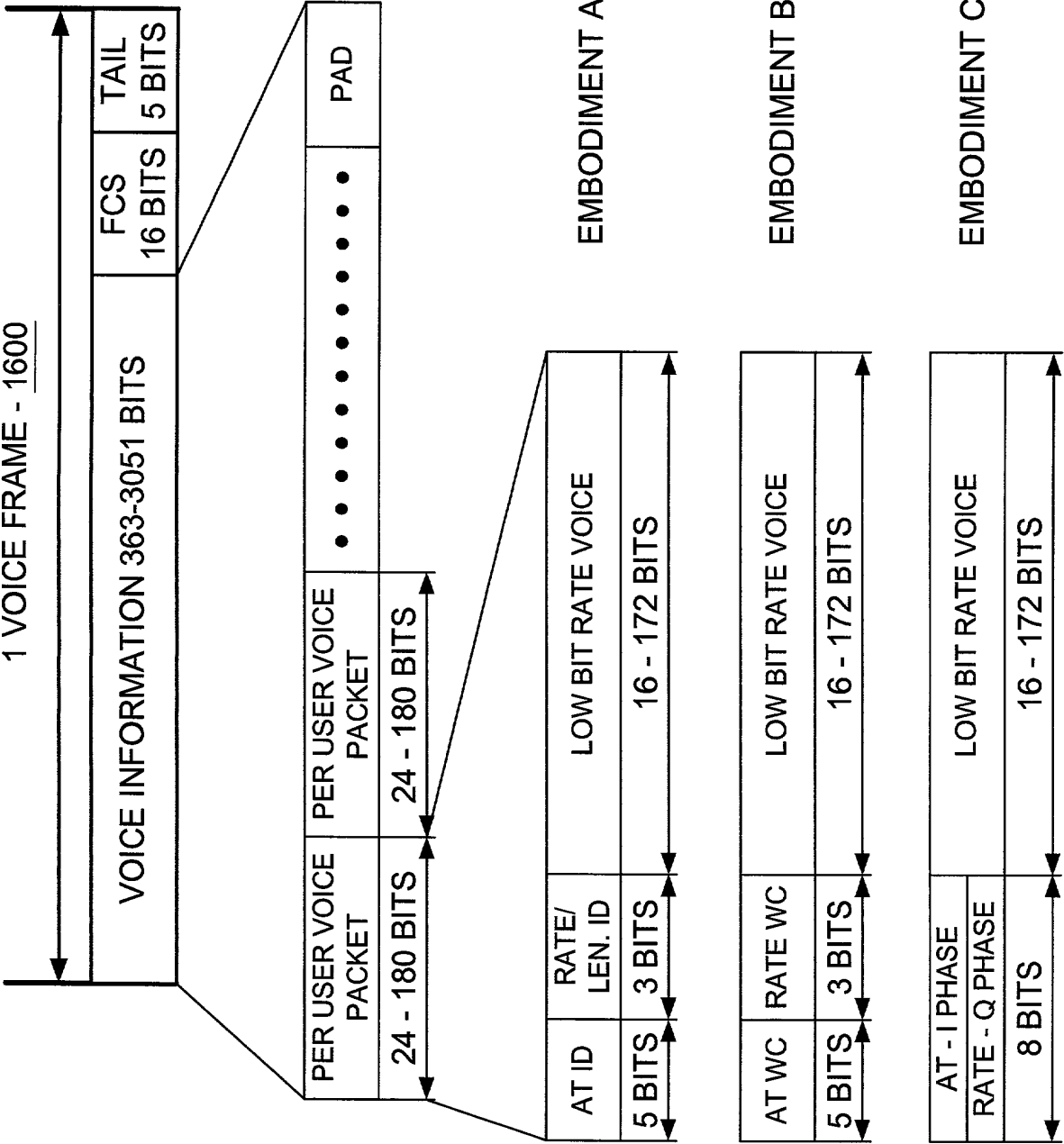


FIG. 16

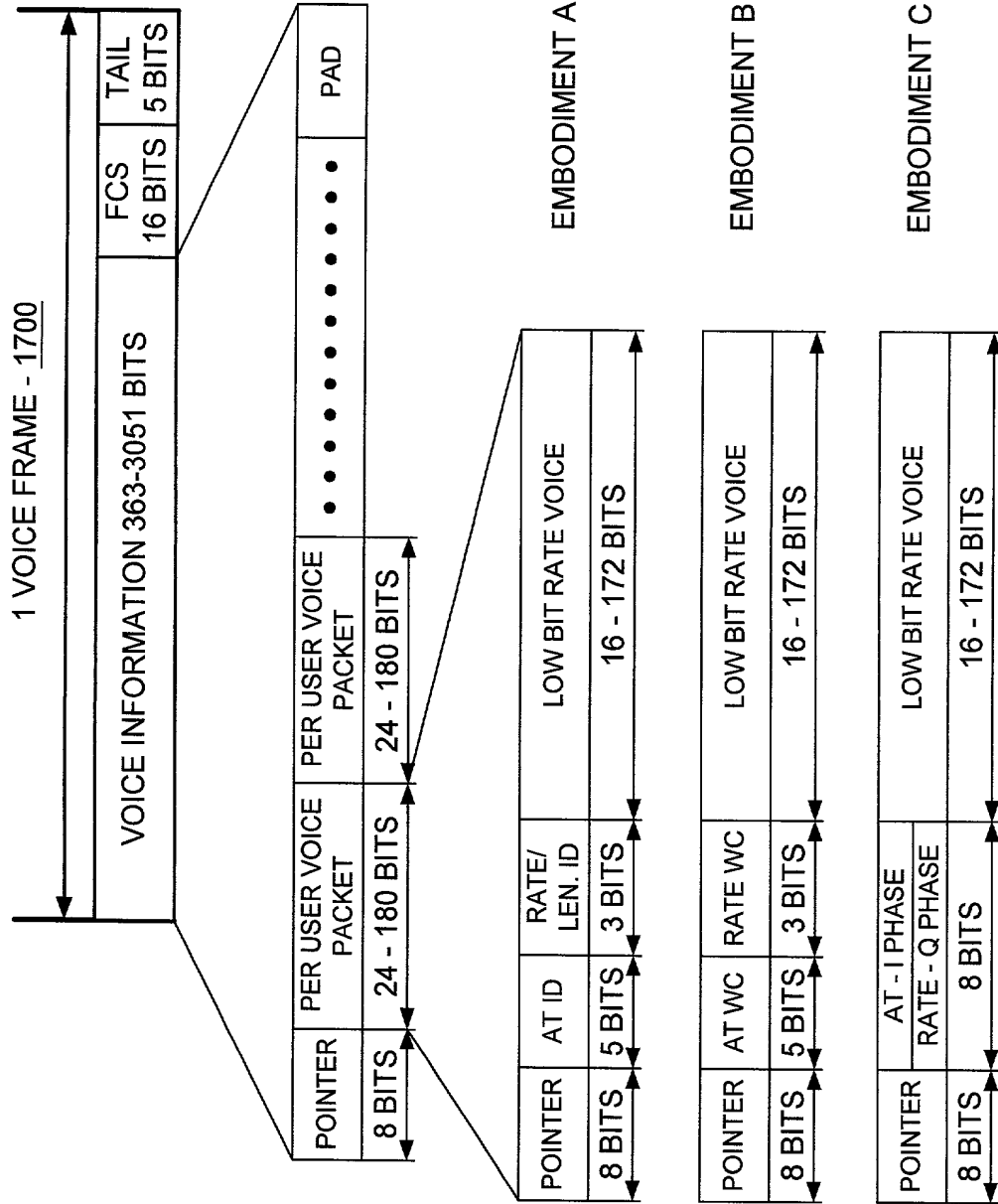


FIG. 17

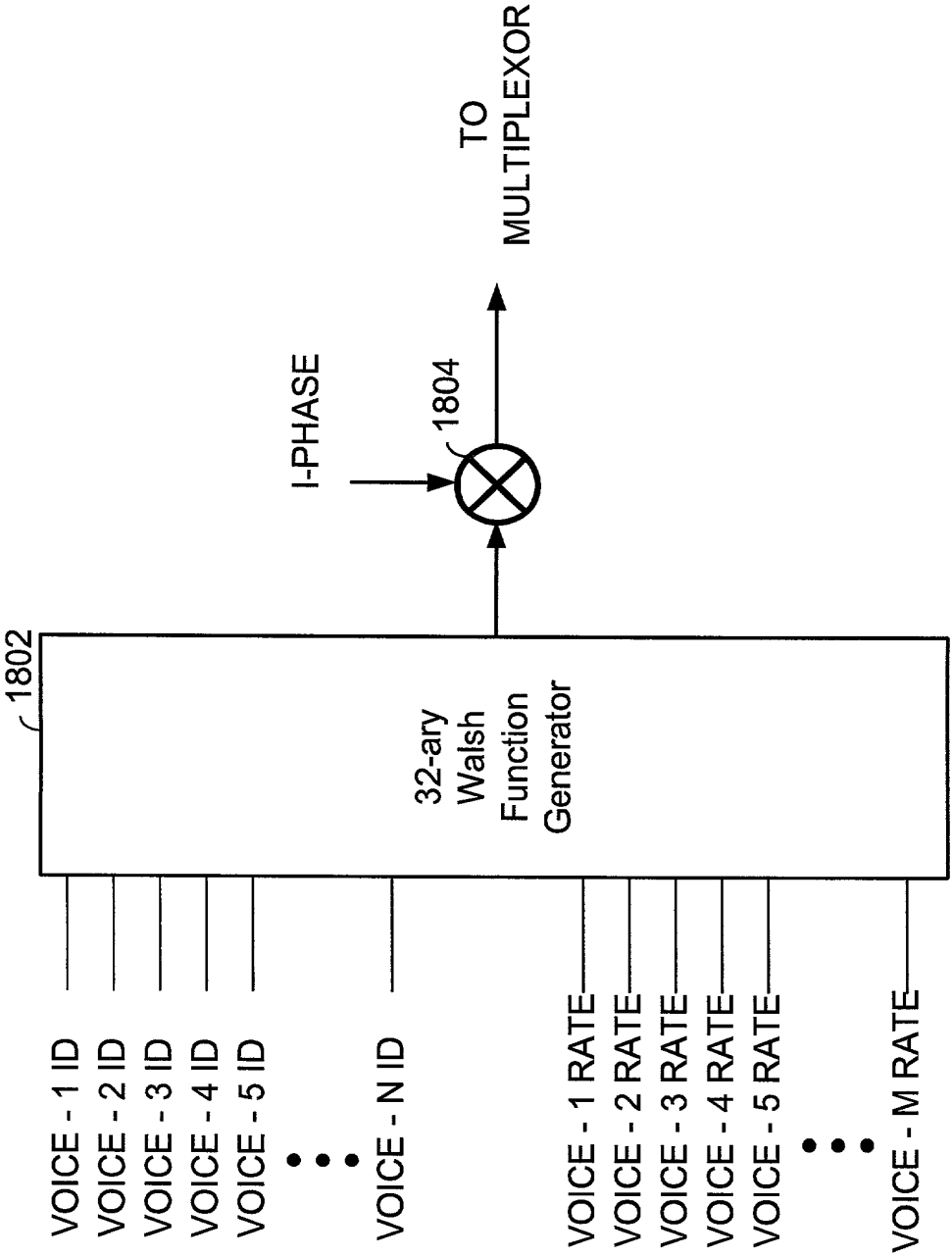


FIG. 18

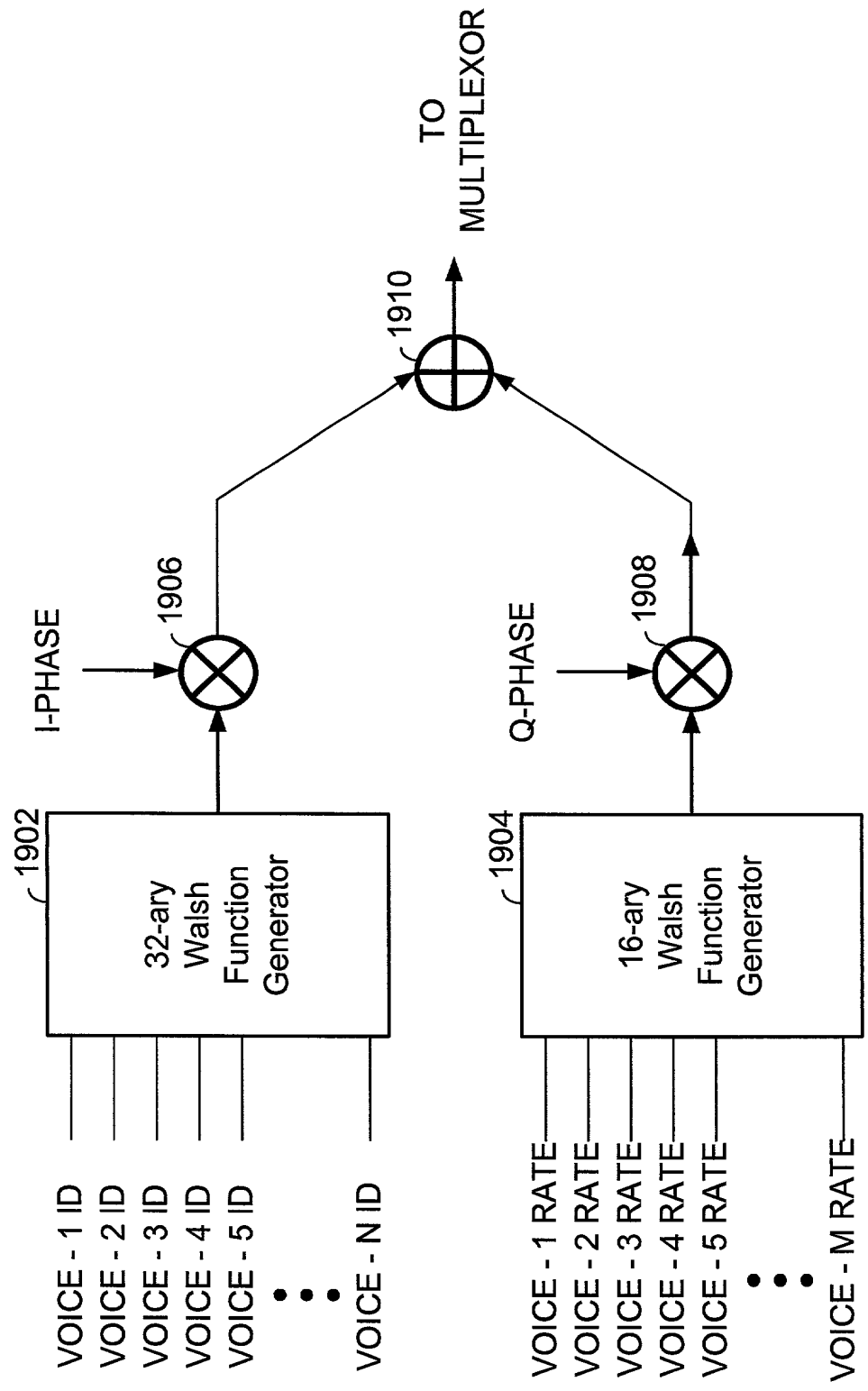


FIG. 19

DATA RATE (KBPS)	76.8	102.4	153.6	204.8	307.2	614.4	921.6	1228.8	1843.2	2457.6
BITS PER ENCODER PACKET	384	384	384	768	384	768	1152	1536	2304	3072
SLOTS PER ENCODER PACKET	4	3	2	3	1	1	1	1	1	1
ENCODER PACKET DURATION - MS (CHIPS)	5.0 (6144)	3.75 (4608)	2.5 (3072)	3.75 (4608)	1.25 (1536)	1.25 (1536)	1.25 (1536)	1.25 (1536)	1.25 (1536)	1.25 (1536)
PREAMBLE PUNCTURE DURATION - CHIPS	512	384	256	192	128	64	64	64	64	64
CODE RATE	1/4	1/4	1/4	1/4	1/4	1/4	1/4	1/4	1/4	1/4
MODULATION TYPE	QPSK	QPSK	QPSK	QPSK	QPSK	QPSK	QPSK	QPSK	8 PSK	16 PSK (QAM)

FIG. 20